

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

Claim 1 (currently amended): An image capture system, comprising[[],]:

a plurality of rows of pixels, each row comprising:

a reset line for providing a reset signal;

a plurality of pixels, each pixel comprising:

a first FET having a gate terminal coupled to [[a]] ~~the~~ reset line for providing a reset signal, a drain terminal coupled to a supply voltage, and a source terminal coupled to a readout node; and

a photodetector coupled between a first ground and the source terminal of the first FET readout node;

a first switching device selectively coupled to one of the reset line lines in the rows of pixels; and

a reference voltage source coupled between a second ground and one of the reset line lines via the first switching device, wherein the reference voltage source generates a ground referenced reset voltage that is independent of the supply voltage and the first and second grounds have the same potential.

Claim 2 (currently amended): The image capture system of claim 1, further comprising an operational amplifier buffer comprising (1) an output coupled between by the switching device to one of the reset line and lines, (2) a non-inverting input coupled to the reference voltage source to receive the reset voltage, and (3) an inverting input coupled to the output in a feedback loop, wherein the feedback loop does not pass through the readout node.

Claim 3 (original): The image capture system of claim 1, wherein the first FET further comprises an n-channel enhancement mode MOSFET.

Claim 4 (currently amended): The image capture system of claim 1, wherein the ~~ground referenced~~ reset voltage is greater than the supply voltage.

Claim 5 (currently amended): The image capture system of claim 1, further comprising a second FET having a gate terminal coupled to the ~~source terminal of the first FET~~ readout node and a drain terminal coupled to the supply voltage.

Claim 6 (currently amended): The image capture system of claim 5, further comprising a third FET having a gate terminal coupled to a row select line, a source terminal coupled to a column line, and a drain terminal coupled to a ~~drain~~ source terminal of the second FET.

Claims 7 to 13 (canceled).

Claim 14 (currently amended): The apparatus image capture system of claim [[13]] 6, wherein the switching device comprises a multiplexer.

Claim 15 (currently amended): A method comprising:

providing a first reset signal to a pixel row of pixels, the first reset signal being ~~equal to derived from~~ a reference voltage that is independent of a supply voltage;

resetting pixels in the pixel row of pixels in response to the first reset signal ~~using a supply voltage, the supply voltage being different from the reference voltage~~;

reading a first plurality of voltage value values generated at the ~~pixel~~ the pixels following a light exposure interval;

providing a second reset signal to the pixel row of pixels, the second reset signal being ~~equal to derived from~~ the reference voltage;

reading a second plurality of voltage value values from the ~~pixel~~ pixels; and

generating a plurality of pixel value values using the first and the second pluralities of voltage values.

Claim 16 (currently amended): The method of claim 15, wherein the plurality of pixel value equals values equal the corresponding second plurality of voltage value values minus the corresponding first plurality of voltage value values.

Claim 17 (currently amended): The method of claim 15, wherein the first plurality of voltage value is values are approximately proportional to [[a]] light intensity intensities detected by the pixel pixels during the light exposure interval.

Claim 18 (currently amended): The method of claim 15, further comprising repeating the providing a first reset signal ~~to a pixel~~, reading a first plurality of voltage value values, providing a second reset signal ~~to the pixel~~, reading a second plurality of voltage values, and generating a plurality of pixel values for a plurality of pixels another row of pixels.

Claim 19 (original): The method of claim 15, wherein the generating is performed by a column circuit.

Claim 20 (currently amended): The method of claim 15 wherein the reading a first plurality of voltage value values comprises exposing [[a]] photodiode photodiodes to incident light.

Claim 21 (new): The image capture system of claim 6, further comprising an operational amplifier comprising (1) an output coupled by the switching device to one of the reset lines, (2) a non-inverting input coupled to the reference voltage source to receive the reset voltage, and (3) an inverting input coupled to the output in a feedback loop, wherein the feedback loop does not pass through the readout node.